

Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. <b>239415US2S</b>		SERIAL NO. <b>10/602,618</b>	
LIST OF REFERENCES CITED BY APPLICANT				APPLICANT <b>Takanori TAMAI</b>			
				FILING DATE <b>June 25, 2003</b>		GROUP <b>2825</b>	
<b>U.S. PATENT DOCUMENTS</b>							
EXAMINER INITIAL	NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE	
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						
<b>FOREIGN PATENT DOCUMENTS</b>							
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES NO		
<i>W</i>	AO	WO 02/084538 A1	10/24/2002	WIPO			
	AP						
	AQ						
	AR						
	AS						
	AT						
	AU						
	AV						
<b>OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)</b>							
<i>W</i>	AW	Mark G. ARNOLD, et al., "A Synthesis Preprocessor that Converts Implicit Style Verilog Into One-Hot Designs", IEEE, VERILOG HDL Conference, March 31, 1997, pages 38-45					
	AX						
	AY						
	AZ						
Examiner <i>W</i>				<input type="checkbox"/> Additional References sheet(s) attached Date Considered <b>9/12/05</b>			
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							